

1AP20 Rec'd FCT/PTO 22 FEB 2006

PTO/SB/08a (08-03)

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First Named Inventor

Art Unit

Examiner Name

Attorney Docket Number

Alexandros Tourapis, et al.

PU040213

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1

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2

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Application Number	10/569319
Filing Date	
First Named Inventor	Alexandros Tourapis
Art Unit	
Examiner Name	
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	FLIERL M. et al.: "A locally optimal design algorithm for block-based multi-hypothesis motion-compensated prediction" DATA COMPRESSION CONFERENCE, 1998, DCC 98. PROCEEDINGS SNOWBIRD, UT, USA 30 MAR 1, APRIL 1998, LOS ALAMITOS, CA USA IEEE COMPUT. SOC. US, 30 MARCH 1998, PGS 239-248.	
	AB	AL-MUALLA M. et al.: "Multiple-reference temporal error concealment" ISCAS 2001. PROCEEDINGS OF THE 2001 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS. SYDNEY, AUSTRALIA, MAY 6-9, 2001 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, NEW YORK, NY IEEE, US VOL. 1 OF 5, 6 MAY 2001, PGS 149-152	
	AC	WIEGAND T. et al.: "Overview of the H.264/AVC Video Coding Standard" IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO TECHNOLOGY, IEEE INC. NEW YORK, US vol. 13, no. 7, July 2003, pages 560-576.	
	AD	SCHWARZ, HEIKO; WIEGAND, THOMAS: "CORE EXPERIMENT RESULTS ON IMPROVED MACROBLOCK PREDICTION MODES" ITU STUDY GROUP 16, 29 January 2002, 1 February 2002, pgs 1-12	
	AE	SCHWARZ, HEIKO; WIEGAND, THOMAS: "TREE STRUCTURED MACROBLOCK PARTITION" ITU STUDY GROUP 16, 4-6 December 2001, pgs 1-9	
		Copy of Search Report Dated November 16, 2004	

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